

AUTONOMOUS END SEMESTER EXAMINATIONS – MAY 2023 TIME TABLE
ME (VLSI Design) Programme (2023 Batch)

Timings FN: 09.30 AM – 12.30 PM

Semester – II

Date / Day	Session	Course Code / Course Name
05.06.2024 Wednesday	FN	23VLR203 Advanced Device Modeling
06.06.2024 Thursday	FN	23VLR209 VLSI Signal Processing
07.06.2024 Friday	FN	23VLR211 Design of Semiconductor Memories
08.06.2024 Saturday	FN	23VLR208 VLSI Design Techniques
10.06.2024 Monday	FN	23VLR210 Low Power VLSI Design
11.06.2024 Tuesday	FN	23VLP205 Nanoscale Devices and Circuit Design
12.06.2024 Wednesday	FN	23VLP208 Mixed Signal VLSI Design



T.K. Senthuramalingam
5/4/2024

CONTROLLER OF EXAMINATIONS

Controller of Examinations
 Karpagam College of Engineering
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